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PATENT NUMBER and
ISSUE DATE

APPL NUM 10092298	FILING DATE 03/07/2002	CLASS 348	SUBCLASS 76.2	GAU 2612	EXAMINER FRAN
**APPLICANTS: Kameyama Isao; <div style="margin-left: 300px;">361</div> <div style="margin-left: 450px;">2839</div> <div style="margin-left: 550px;">Prasad</div>					
**CONTINUING DATA VERIFIED:					
** FOREIGN APPLICATIONS VERIFIED: JAPAN 2001-66665 03/09/2001					
PG-PUB <input type="checkbox"/> DO NOT PUBLISH		RESCIND <input type="checkbox"/>			
Foreign priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no		35 USC 119 conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no		ATTORNEY DOCKET NO	
Verified and Acknowledged Examiners's initials <i>chr</i>				020295	
TITLE : Junction circuit for auxiliary device module and auxiliary device module <small>U.S. DEPT. OF COMM./PAT. & TM-PTO-436L (Rev. 12-94)</small>					

NOTICE OF ALLOWANCE MAILED		Assistant Examiner	CLAIMS ALLOWED	
			Total Claims	Print Claim for O.G.
ISSUE FEE		Primary Examiner	DRAWING	
Amount Due	Date Paid		Sheets Drwg.	Figs. Drwg.
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE	Application Examiner	
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